



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

GARG *et al.*

Appl. No.: 10/060,086

Filed: January 31, 2002

For: **RISC Microprocessor Architecture
Implementing Multiple Typed
Register Sets**

Confirmation No.: 6414

Art Unit: 2154

Examiner: Larry D. Donaghue

Atty. Docket: 2047.0600007

Fourth Supplemental Information Disclosure Statement

Mail Stop Amendment

Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

Sir:

Listed on accompanying IDS Forms are documents that may be considered material to the examination of this application, in compliance with the duty of disclosure requirements of 37 C.F.R. §§ 1.56, 1.97 and 1.98.

Where the publication date of a listed document does not provide a month of publication, the year of publication of the listed document is sufficiently earlier than the effective U.S. filing date and any foreign priority date so that the month of publication is not in issue. Applicants have listed publication dates on the attached IDS Forms based on information presently available to the undersigned. However, the listed publication dates should not be construed as an admission that the information was actually published on the date indicated.

Applicants reserve the right to establish the patentability of the claimed invention over any of the information provided herewith, and/or to prove that this information may

not be prior art, and/or to prove that this information may not be enabling for the teachings purportedly offered.

This statement should not be construed as a representation that a search has been made, or that information more material to the examination of the present patent application does not exist. The Examiner is specifically requested not to rely solely on the material submitted herewith.

Filing under 37 C.F.R. § 1.97(b). This Information Disclosure Statement is being filed concurrently with the filing of a request for continued examination under 37 C.F.R. § 1.114. No statement or fee is required.

Copies of documents NPL1-NPL4 are submitted. However, in accordance with 37 C.F.R. § 1.98(a)(2), a copy of the U.S. patent cited on the attached IDS Forms is not submitted.

It is expected that the examiner will review the prosecution and cited art in the parent application no. 09/840,026 in accordance with MPEP 2001.06(b), and indicate in the next communication from the office that the art cited in the earlier prosecution history has been reviewed in connection with the present application.

It is respectfully requested that the Examiner initial and return a copy of the enclosed IDS Forms, and indicate in the official file wrapper of this patent application that the documents have been considered.

The U.S. Patent and Trademark Office is hereby authorized to charge any fee deficiency, or credit any overpayment, to our Deposit Account No. 19-0036.

Respectfully submitted,

STERNE, KESSLER, GOLDSTEIN & FOX P.L.L.C.



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Substitute for form 1449/PTO

**FOURTH SUPPLEMENTAL
INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Sheet 1 of 1

Complete if Known

Application Number	10/060,086
Filing Date	January 31, 2002
First Named Inventor	Sanjiv GARG
Art Unit	2154
Examiner Name	Larry D. Donaghue
Attorney Docket Number	2047.0600007

U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if Known)			
	US1	4,926,323	05-15-1990	Baror <i>et al.</i>	
	US2				
	US3				
	US4				
	US5				
	US6				
	US7				
	US8				
	US9				
	US10				
	US11				
	US12				
	US13				
	US14				
	US15				
	US16				
	US17				
	US18				
	US19				
	US20				

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ Number ⁴ Kind Code ⁵ (if known)				
	FP1					
	FP2					
	FP3					
	FP4					
	FP5					
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Examiner Signature		Date Considered	
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This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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Substitute for form 1449/PTO				Complete if Known	
FOURTH SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)				Application Number	10/060,086
				Filing Date	January 31, 2002
				First Named Inventor	Sanjiv GARG
				Art Unit	2154
				Examiner Name	Larry D. Donaghue
				Attorney Docket Number	2047.0600007
Sheet	1	of	1		

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T ²
	NPL1	Intrater <i>et al.</i> , "A Superscalar Microprocessor," <i>IEEE Proceedings of the 17th Convention of Electrical & Electronics Engineers In Israel</i> , pp. 267-270 (March 1991).	
	NPL2	McGeady, "The i960CA SuperScalar Implementation of the 80960 Architecture," <i>IEEE</i> , pp. 232-240 (1990).	
	NPL3	Melear, "The Design of the 88000 RISC Family," <i>IEEE Micro</i> , Vol. 9, No.2, pp. 26-38 (April 1989).	
	NPL4	Popescu <i>et al.</i> , "The Metaflow Architecture," <i>IEEE Micro</i> , Vol. 11, No.3, pp. 10-13, 63-73 (June 1991).	
	NPL5		
	NPL6		
	NPL7		
	NPL8		
	NPL9		
	NPL10		

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Examiner Signature		Date Considered	
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